

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Online Class Time Table
II B.Tech I Semester ECE Section-C

w.e.f: 21.12.2020

Time(Hrs)	10:00-11:00	11:15-12:15	12:15-14:00	14:00-15:00	15:15-16:15
Day	1	2	3	4	5
Monday	SS	COI	L U N C H	EDC	PTSP
Tuesday	NATL	PTSP		DSD	COI
Wednesday	DSD	SS		EDC	NATL
Thursday	COI	NATL		DSD	EDC LAB
Friday	PTSP	DSD		EDC	SS
Saturday	EDC	SS		BS LAB	DSD LAB

S.No	Subject		Faculty	Dept / Faculty
1	EDC	Electronic Devices and Circuits	A Navila	ECE
2	NATL	Network Analysis and Transmission Lines	Saba Sultana	ECE
3	DSD	Digital System Design	Nikhat Parvin	ECE
4	SS	Signals and Systems	G Srilakshmi	ECE
5	PTSP	Probability Theory & Stochastic Processes	SVMG Phani Kumar	ECE
6	COI	Constitution of India	Shaguftha Nishat	BA
7	EDC LAB	Electronic Devices and Circuits Lab	A Navila	ECE
8	DSD LAB	Digital System Design Lab	Saba Sultana	ECE
9	BS LAB	Basic Simulation Lab	G Srilakshmi	ECE

Incharge
G Srilakshmi / Nikhat Parvin

HoD
Dr N Shribala

Principal
Dr J Madhavan